

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Sarkissian et al. Serial No.: 10/828,776 Filed: April 20, 2004 Title: STATE PROCESSOR FOR PATTERN MATCHING IN A NETWORK MONITOR DEVICE	Group Art Unit: 1762 Examiner:
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL: INFORMATION DISCLOSURE STATEMENT

Dear Commissioner:

Transmitted herewith are:

- X An Information Disclosure Statement for the above referenced patent application, together with PTO form 1449 and a copy of each reference cited in form 1449, except for U.S. Patents and patent applications.
- ___ A payment for petition fees.
- ___ Return postcard.
- X The commissioner is hereby authorized to charge payment of any missing fee associated with this communication or credit any overpayment to Deposit Account 50-0292.

A DUPLICATE OF THIS TRANSMITTAL IS ATTACHED

Respectfully submitted,

Date: June 1, 2006

/Dov Rosenfeld/ #38687

Dov Rosenfeld
Attorney/Agent for Applicant(s)
Reg. No. 38687

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INFORMATION DISCLOSURE STATEMENT

Dear Commissioner:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of international application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:
 ☐ Certification under 37 CFR 1.97(e), or
 ☐ a \$180.00 fee under 37 CFR 1.17(p)
(After the CFR 1.97(b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97(d) together with a:
 ☐ Certification under 37 CFR 1.97(e), and
 ☐ a petition under 37 CFR 1.97(d)(2)(ii), and
 ☐ a \$130.00 petition fee set forth in 37 CFR 1.17(i)(1).
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

X Applicant(s) submit herewith Form PTO 1449-Information Disclosure Citation together with copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

X (Certification under 37 C.F.R. 1.97 (e)) Each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.

It is expressly requested that the cited information be made of record in the application and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Respectfully submitted,

Date: June 1, 2006

/Dov Rosenfeld/ #38687

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				APPLICANT Sarkissian et al.					
				FILING DATE 20 Apr 2004		GROUP 1762			
(Use several sheets if necessary)									
U.S. PATENT DOCUMENTS									
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE (IF APPROPRIATE)		
	AA	5,430,709	Jul. 4, 1995	Galloway	370	13	Feb. 8, 1994		
	AB	5,793,954	Aug. 11, 1998	Baker et al.	395	200.8	Dec. 20, 1995		
	AC	5,249,282	Sep. 28, 1993	Segers	395	425	Nov. 21, 1990		
	AD	5,717,916	Feb. 10, 1998	Verma	395	607	Sep. 30, 1994		
	AE	5,369,605	Nov. 29, 1994	Parks	364	715.09	Jul. 7, 1993		
	AF	5,060,143	Oct. 22, 1991	Lee	364	200	Aug. 10, 1988		
	AG								
FOREIGN PATENT DOCUMENTS									
		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO		
	AH								
	AI								
OTHER DISCLOSURES (including Author, Title, Date, Pertinent Pages, Place of Publication, Etc.)									
	AJ	Rona J. Prufer, "Network Advisor Protocol Analysis: Decodes," Hewlett-Packard Journal, Hewlett-Packard Co. Palo Alto, CA, vol. 43, no. 5, October 1, 1992, pp. 34-40.							
	AK	Walter Gora, "Universal Network Monitoring based on FAN.1," Raviv J. International Council For Computer Communication, Computer Communication Technologies For The 90's, Tel Aviv, Oct. 30-Nov. 3, 1988, Proc. Of The International Conference On Computer Communication, Amsterdam, Elsevier, NL, conf. 9, October 30, 1988, pp. 104-114.							
	AL	G. Giacolone, R. Busch, F. Creed, A. Davidovich, S. Divakaruni, C. Drake, C. Ematrudo, J. Fifield, M. Hodges, W. Howell, P. Jenkins, M. Kozycszak, C. Miller, T. Obremski, C. Reed, G. Rohrbach, M. Vincent, T. von Reyn and J. Zimmermann, "A 1MB, 100MHz Integrated L2 Cache Memory with 128b Interface and ECC Protection," Solid-State Circuits Conference, 1996. Digest of Technical Papers, 42 nd ISSCC., 1996 IEEE International, San Francisco, CA, Feb. 8-10, 1996, New York, NY, IEEE, US, February 8, 1996, pp. 370-371, 475.							
EXAMINER				DATE CONSIDERED					
*EXAMINER: initial if citation considered, whether or not citation is in conformance with MPEP 608. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to Applicant.									

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	BA						
	BB						
	BC						
	BD						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO
	BE						
	BF						
OTHER DISCLOSURES (Including Author, Title, Date, Pertinent Pages, Price of Publication, Etc.)							
	BG	David C. Feldmeier, "Improving Gateway Performance With A Routing-Table Cache," Networks: Evolution Or Revolution? New Orleans, Mar. 27-31, 1988, Proceedings of the Annual Joint Conference of the Computer and Communications Societies. (Infocom), New York, IEEE, US, conf. 7, March 27, 1988, pp. 306-307.					
	BH	J.K. Peir, W.W. Hsu, H. Young and S. Ong, "Fast Cache Access with Full-Map Block Directory," Proceedings of the 1997 IEEE International Conference on Computer Design: VLSI In Computers and Processors, 1997, ICCD '97, Austin, TX, Oct. 12-15 1997, Los Alamitos, CA, IEEE Computer Society, October 12, 1997, pp. 578-579.					
	BI	J.H. Park and K.M. George. "Efficient Parallel Hardware Algorithms For String Matching," Microprocessors and Microsystems, IPC Business Press Ltd., London, GB, vol. 23, no. 3, October 1, 1999, pp. 155-168.					
	BJ						
	BK						
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